

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|---|---|------------------|---------|------------------|
| S1 | 2 | ((("6282556") or ("6036350"))).PN. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/02/07 07:50 |
| S2 | 241 | (712/222).CCLS. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/05/14 09:19 |
| S3 | 6 | (media adj1 processor) near4 (float\$3 adj1 point) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/02/07 07:50 |
| S4 | 57 | (media) near4 (float\$3 adj1 point) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/02/07 07:50 |
| S5 | 245 | (712/222).CCLS. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:11 |
| S6 | 0 | (sum near4 absolute near4 differenc3\$1) near4 (intermediate\$2) near4 (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:11 |
| S7 | 1 | ((sum near4 absolute near4 differenc3\$1) or SAD or SABD) near4 (intermediate\$2 or middl\$3 or temporar\$3) near4 (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:12 |
| S8 | 1 | ((sum near4 absolute near4 differenc3\$1) or SAD or SABD) with (intermediate\$2 or middl\$3 or temporar\$3) with (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:12 |
| S9 | 1 | ((sum near4 absolute near4 differenc3\$1) or SAD or SABD) same (intermediate\$2 or middl\$3 or temporar\$3) same (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:13 |
| S10 | 1 | ((sum near4 differenc3\$1) or SAD or SABD) same (intermediate\$2 or middl\$3 or temporar\$3) same (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:16 |
| S11 | 19574 | (intermediate\$2 or middl\$3 or temporar\$3) same (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:13 |

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| S12 | 588 | (intermediate\$2 near4 (value\$1 or data or number\$1)) same (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:13 |
| S13 | 125 | (intermediate\$2 near4 (value\$1 or data or number\$1)) near4 (saturat\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:15 |
| S14 | 0 | ((intermediate\$2 near4 (value\$1 or data or number\$1)) near4 (saturat\$3)) and ("712"/).ccls. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:34 |
| S15 | 7 | ((sum near4 differenc3\$1) or SAD or SABD) and ((intermediate\$2 or middl\$3 or temporar\$3) near4 (saturat\$3)) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:21 |
| S16 | 45 | ((sum near4 differenc3\$1) or SAD or SABD) and ((intermediate\$2 or middl\$3 or temporar\$3) with (saturat\$3)) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:29 |
| S17 | 77 | ((sum near4 differenc3\$1) or SAD or SABD) and ((intermediate\$2 or middl\$3 or temporar\$3) same (saturat\$3)) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:29 |
| S18 | 77 | ((sum near8 differenc3\$1) or SAD or SABD) and ((intermediate\$2 or middl\$3 or temporar\$3) same (saturat\$3)) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:29 |
| S19 | 0 | (intermediate adj1 value\$1) near4 (saturation) near4 overflow | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:35 |
| S20 | 36 | (intermediate adj1 value\$1) near4 (saturation) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:39 |
| S21 | 13 | (diefendorff).in. and (motorola).as. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:47 |
| S22 | 0 | ("61344090").PN. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:42 |
| S23 | 4 | (sooch).in. and (motorola).as. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2005/07/21 16:47 |

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|-----|-----|--|---|----|-----|------------------|
| S24 | 254 | (712/222).CCLS. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:20 |
| S25 | 737 | (absolute near4 difference\$1) near4 (select\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:20 |
| S26 | 224 | (absolute adj1 difference\$1) near4 (select\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:20 |
| S27 | 0 | (absolute adj1 difference\$1) near4 (selecting near4 positive near4 result\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:21 |
| S28 | 0 | (absolute adj1 difference\$1) with (selecting near4 positive near4 result\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:21 |
| S29 | 0 | (absolute adj1 difference\$1) with (select\$3 near4 positive near4 result\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:21 |
| S30 | 1 | (absolute adj1 difference\$1) with (select\$3 with positive with result\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:22 |
| S31 | 6 | (absolute near4 difference\$1) same (select\$3 near4 positive near4 result\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:31 |
| S32 | 31 | (absolute near4 difference\$1) same (select\$3 with positive with result\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 09:32 |
| S33 | 36 | (Sun).as. and (watkins).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 12:36 |
| S34 | 0 | ((Sun).as. and (watkins).in.) and (visual adj1 instruction\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 12:37 |
| S35 | 0 | ((Sun).as. and (watkins).in.) and (visual near4 instruction\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 12:37 |

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|-----|-----|---|---|----|-----|------------------|
| S36 | 3 | (Sun).as. and ((graphic\$1 adj1 process\$3) same integrate\$1) and (visual near4 instruction near4 set) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 12:39 |
| S37 | 2 | (Sun).as. and (graphic\$1 same integrate\$1 same (memor\$3 adj1 control\$4)) and (visual near4 instruction near4 set) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 12:42 |
| S38 | 1 | ("5734874").PN. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 12:43 |
| S39 | 1 | ("5996066").PN. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/06 12:43 |
| S40 | 1 | ("6828556").PN. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/09 08:53 |
| S41 | 1 | ("6282556").PN. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/01/09 08:53 |
| S42 | 263 | (712/222).CCLS. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:24 |
| S43 | 26 | (vector\$1 near4 absolute near4 difference\$1) same register\$1 | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:52 |
| S44 | 600 | (vector\$1 near4 absolute near4 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:52 |
| S45 | 62 | vector\$1 near4 absolute near4 difference\$1 near4 (select\$4 or portion\$3 or part\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:54 |
| S46 | 57 | vector\$1 near4 absolute near4 difference\$1 near4 (select\$4 or portion\$3) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:54 |
| S47 | 49 | vector\$1 near4 absolute near4 difference\$1 near4 (select\$4) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:54 |

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| S48 | 0 | vector\$1 near4 absolute near4 difference\$1 near4 (select\$4) near4 instruction\$1 | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:54 |
| S49 | 0 | (vector\$1 near4 (select\$4)) with (absolute near4 difference\$1 near4 instruction\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:55 |
| S50 | 11 | (vector\$1 near4 (select\$4)) same (absolute near4 difference\$1 near4 instruction\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:55 |
| S51 | 76 | (vector\$1 near4 select\$4) with (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/20 14:56 |
| S52 | 26 | (vector\$1 near4 select\$4) near4 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/21 11:59 |
| S53 | 5 | (vector\$1 near4 (portion\$1 or part\$1)) near4 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/21 11:59 |
| S54 | 336 | (vector\$1) near4 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:05 |
| S55 | 19 | (vector\$1) adj1 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2006/07/21 12:00 |
| S56 | 268 | (712/222).CCLS. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:04 |
| S57 | 0 | ((trivedi-sushma\$) and (bratt-joseph\$) and (vaughn-arnold\$) and (athas-william\$) and (chen-jason\$)). in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/05/14 09:21 |
| S58 | 0 | ((trivedi-sushma\$) and (bratt-joseph\$) and (arnold-vaughn\$) and (athas-william\$) and (chen-jason\$)). in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:09 |

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|-----|-----|--|---|----|-----|------------------|
| S59 | 0 | ((trivedi\$) and (bratt\$) and (arnold\$) and (athas\$) and (chen-jason\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:10 |
| S60 | 0 | ((trivedi\$) and (bratt\$) and (arnold\$) and (athas\$) and (chen)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:11 |
| S61 | 714 | ((trivedi\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:11 |
| S62 | 23 | ((trivedi-sushma\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:11 |
| S63 | 14 | ((trivedi-sushma\$) and (bratt-joseph\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:11 |
| S64 | 7 | ((trivedi-sushma\$) and (bratt-joseph\$) and (arnold-vaughn\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:12 |
| S65 | 0 | ((trivedi-sushma\$) and (bratt-joseph\$) and (arnold-vaughn\$) and (athas-william\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:12 |
| S66 | 7 | ((trivedi-sushma\$) and (bratt-joseph\$) and (arnold-vaughn\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/01/07 21:12 |
| S67 | 277 | (712/222).CCLS. | US-PGPUB; USPAT | OR | OFF | 2007/05/14 09:19 |
| S68 | 12 | (vector\$1 near4 (select\$4)) same (absolute near4 difference\$1 near4 instruction\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/05/14 09:20 |
| S69 | 0 | ((trivedi\$) and (bratt\$) and (arnold\$) and (athas\$) and (chen-jason\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/05/14 09:21 |
| S70 | 199 | ((trivedi-sushma\$) or (bratt-joseph\$) or (vaughn-arnold\$) or (athas-william\$) or (chen-jason\$)).in. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/05/14 09:22 |

EAST Search History

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| S71 | 33 | ((trivedi-sushma\$) or (bratt-joseph\$) or (vaughn-arnold\$) or (athas-william\$) or (chen-jason\$)).in. and (apple\$).as. | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/05/14 09:22 |
| S72 | 285 | (712/222).CCLS. | US-PGPUB; USPAT | OR | OFF | 2007/10/29 18:52 |
| S73 | 23 | (vector\$1) adj1 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/10/29 18:52 |
| S74 | 14 | (vector\$1 near4 (select\$4)) same (absolute near4 difference\$1 near4 instruction\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/10/29 18:54 |
| S75 | 34 | (vector\$1 near4 select\$4) near4 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/10/29 18:54 |
| S76 | 5 | (vector\$1 near4 (portion\$1 or part\$1)) near4 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/10/29 18:55 |
| S77 | 34 | (vector\$1 near4 select\$4) near4 (absolute adj1 difference\$1) | US-PGPUB; USPAT; EPO; JPO; IBM_TDB | OR | OFF | 2007/10/29 18:55 |

Scholar All articles - Recent articles Results 11 - 20 of about 533 for Vector + "absolute difference" + instruction + register. (0.08 seconds)

All Results

M Tremblay

L Kohn

G Maturana

A Prabhu

G Zyner

Design of application-specific instruction-set processors for multi-media, using a retargetable ...

W Geurts, G Goossens, D Lanneer, J Van Praet - Proc. Intl. Signal Proc. Conf.(GSPx), Santa Clara, Oct, 2005 - retarget.com

... An extra **register** file is added, consisting of ... Also a separate **vector sum instruction**

is provided ... functions such as vaddff() (**vector absolute difference**) can be ...

Cited by 2 - Related Articles - View as HTML - Web Search

Method and an apparatus for providing the **absolute difference** of unsigned values - all 3 versions »

LM Mennemeier, AD Peleg, C Gottlieb - US Patent 5,742,529, 1998 - Google Patents

... residual information 302 and motion **vector** 312 to ... one embodiment of how an **absolute difference** calculation is ... to the data on which an instruction operates or ...

Cited by 3 - Related Articles - Web Search

VIS speeds new media processing - all 4 versions »

M Tremblay, JM Narayanan, VL He - Micro, IEEE, 1996 - ieeexplore.ieee.org

... several widely used media-processing algorithms: separable convolution, **vector dot product** ... **Instruction** operands in the floating-point **register** file can ...

Cited by 196 - Related Articles - Web Search - BL Direct

Efficient implementation of MPEG-4 video encoder on RISC core - all 6 versions »

RSV Prasad, R Korada, EIP Ltd - Consumer Electronics, IEEE Transactions on, 2003 - ieeexplore.ieee.org

... unrestricted motion **vector**, four motion **vector** etc ... by the field, the **instruction** is executed ... while calculating SAD value using **absolute difference** between pixel ...

Cited by 6 - Related Articles - Web Search - BL Direct

A motion estimation chip for block based MPEG-4 video applications - all 2 versions »

M Abbas, B Talha, S Khan, A Abbas - Multi Topic Conference, 2003. INMIC 2003. 7th International, 2003 - ece.jhu.edu

... the PUs are used to calculate **absolute difference** between two ... Motion **vector** precision to half pel is achieved ... The **instruction** set operates on AGU **register** file ...

Cited by 2 - Related Articles - View as HTML - Web Search

Intel® Wireless MMX (TM) Technology: A 64-Bit SIMD Architecture for Mobile Multimedia - all 2 versions »

NC Paver, BC Aldrich, MH Khan - International Conference on Acoustics, Speech, and Signal ..., 2003 - viola.usc.edu

... This sums the **absolute difference** of the eight corresponding ... WMAX/WMIN **Vector** maximum/minimum selection WMADD ... described the architecture and **instruction** set of ...

Cited by 4 - Related Articles - View as HTML - Web Search

Computational RAM Implementation of Vector Quantization for Image Compression

TM Le, S Panchanathan, M Snelgrove - Proceedings of the IEEE Workshop on Visual Signal Processing ..., 1994 - dissonance.com

... Y global **instruction** ... 1's in **vector** P in figure 5) the closest match of the input **vector** will finally be searched using the **absolute- difference** search ...

Cited by 1 - Related Articles - View as HTML - Web Search

CMOS processor for template-based speech-recognition system - all 2 versions »

W Drews, R Laroia, J Pandel, A Schumacher, A ... - Communications, Speech and Vision, IEE Proceedings 1, 1989 - ieeexplore.ieee.org

... factor **register** file) to the **instruction register** and the ... components of the unknown word **vector** and the ... 9. The **absolute difference** is always calcu- lated so ...

Cited by 1 - Related Articles - Web Search

Fast Color Image Processing Using Quantized Color Instruction Set - all 4 versions »

J Kim, S Bunchua, DS Wills - Information Technology: Coding and Computing (Computers and ..., 2003 - ece.gatech.edu

... Using the ADACC_CRCBY (**absolute-difference- accumulate**) **instruction**, nine ... ADDR ; load image **vector** addr addi r1 ... Then, the MACC_CRCBY **instruction** accumulates its ...

Cited by 1 - Related Articles - View as HTML - Web Search

A vector based fast block motion estimation algorithm for implementation on SIMD architectures

C Duanmu, MO Ahmad, MNS Swamy, A Shatnawi - Circuits and Systems, 2002. ISCAS 2002. IEEE International ..., 2002 - ieeexplore.ieee.org

... algorithm is called the **vector** based fast ... be carried out simultaneously using an SIMD **instruction**. ... of the additions, subtractions, **absolute difference**, and the ...

Cited by 2 - Related Articles - Web Search - BL Direct

◀ Google ▶

Result Page: Previous 1 2 3 4 5 6 7 8 9 10 11 Next

Scholar All articles - [Recent articles](#) Results 1 - 10 of about 2,600 for **Vector + "absolute difference" + register**. (0.20 seconds)

All Results

[F Maes](#)

[A Collignon](#)

[D Vandermeulen](#)

[G Marchal](#)

[P Suetens](#)

Multimodality image registration by maximization of mutual information - all 21 versions »

F Maes, A Collignon, D Vandermeulen, G Marchal, P ... - Medical Imaging, IEEE Transactions on, 1997 - [ieeexplore.ieee.org](#)

... the difference **vector** j 0 3 j . 3 corresponds to the **registration** solution obtained

when no subsampling is applied. maximal **absolutedifference** evaluated over ...

[Cited by 1294](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

Motion **vector detecting device for compensating for movements in a motion picture** - all 2 versions »

S Uramoto, M Suzuki, A Takabatake - US Patent 5,400,087, 1995 - Google Patents

... of a current frame and a circuit for obtaining an **absolute difference** of the ... outputs

of the summation unit to detect a mo -tion-**vector** for the ... fl DATA REGISTER ...

[Cited by 33](#) - [Related Articles](#) - [Web Search](#)

VLSI architecture for block-matching motion estimation algorithm - all 3 versions »

CH Hsieh, TP Lin - Circuits and Systems for Video Technology, IEEE Transactions ..., 1992 - [ieeexplore.ieee.org](#)

... The motion **vector** is deter- mined by the least MAD (u.. S(k, 1) to right- neighbor

PE1 or shift **register** (SR), 3) to calculate the **absolute difference** (AD) value ...

[Cited by 109](#) - [Related Articles](#) - [Web Search](#)

A half-pel precision MPEG2 motion-estimation processor with concurrent three-vector** search** - all 3 versions »

K Ishihara, S Masuda, S Hattori, H Nishikawa, Y ... - Solid-State Circuits, IEEE Journal of, 1995 - [ieeexplore.ieee.org](#)

... surrounding the selected integer-pci**vector** are evaluated. ... units, the matching criterion

is the mean **absolute difference**.... lower-bottom f Side **register** (32 words ...

[Cited by 39](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

Method and apparatus for generating large compound ultrasound image - all 4 versions »

L Weng, AP Tirumalai - US Patent 5,575,286, 1996 - Google Patents

... **absolute-difference** search of the image **registration** method; ... factor for the

minimum-sum-**absolute-difference** 5 search ... derivation of a local **vector** deviation factor..

[Cited by 48](#) - [Related Articles](#) - [Web Search](#)

Consistent image registration - all 11 versions »

GE Christensen, HJ Johnson - Medical Imaging, IEEE Transactions on, 2001 - [ieeexplore.ieee.org](#)

... All of the func- tions , , , , and are (3 1)**vector**- ... **Registration** is defined using

a symmetric similarity cost function that describes the distance ...

[Cited by 119](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

Parameterizable VLSI architectures for the full-search block-matching algorithm

L de Vos, M Stegherr - Circuits and Systems, IEEE Transactions on, 1989 - [ieeexplore.ieee.org](#)

... it is assumed that all **absolute difference** values belonging to ... block, together with

the corre- sponding displacement **vector**. ... feeds data to a **register** from the..

[Cited by 133](#) - [Related Articles](#) - [Web Search](#)

Multiresolution image registration - all 3 versions »

M Corvi, G Nicchiotti - ... Processing, 1995. Proceedings., International Conference on, 1995 - [ieeexplore.ieee.org](#)

... 2) right quadrant shows the **absolutedifference** between the images ... with large rotations,

we tried to **register** a couple ... 90 degrees and the shift **vector** was ten ...

[Cited by 15](#) - [Related Articles](#) - [Web Search](#)

Extension of phase correlation to subpixel registration - all 7 versions »

H Foroosh, JB Zerubia, M Berthod - Image Processing, IEEE Transactions on, 2002 - [ieeexplore.ieee.org](#)

... tege- rated **vector**. ... FOROOSH et al.: EXTENSION OF PHASE CORRELATION TO SUBPIXEL

REGISTRATION ... An example of the variations of the **absolute difference** between a ...

[Cited by 98](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

A video digital signal processor with a **vector-pipeline architecture** - all 4 versions »

K Aono, M Toyokura, T Araki, A Ohtani, H Kodama, K ... - Solid-State Circuits, IEEE Journal of, 1992 - [ieeexplore.ieee.org](#)

... current contents of the instruc- tion **register** (IREG) and ... access two-dimen- sional

image data as a **vector**. ... IN1 ,1N2) - MIN (IN1,1N2) - **absolute difference** IN1 ...

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